

DRD6990M28

Rectifier Diode

Replaces DS5869-2.0 January 2010 – Datasheet DS2904SM28

DS6228-1 February 2018 (LN35173)

FEATURES

- Double Side Cooling
- High Surge Capability

APPLICATIONS

- Rectification
- Free-wheel Diode
- DC Motor Control
- Power Supplies
- Welding
- Battery Chargers

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{DRM} and V _{DRM} V	Conditions		
DRD6990M28 DRD6990M26 DRD6990M24	2800 2600 2400	$\begin{split} T_{vj} &= \text{-}40^{\circ}\text{C to 160°C}, \\ I_{RRM} &= 200\text{mA}, \\ V_{RRM} t_p &= 10\text{ms}, \\ V_{RSM} &= V_{RRM} + 100V \\ \text{respectively} \end{split}$		

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD6990M26 for a 2600V device

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

KEY PARAMETERS

 $\begin{array}{ll} V_{RRM} & 2800V \\ I_{F(AV)} & 8790A \\ I_{FSM} & 95000A \end{array}$

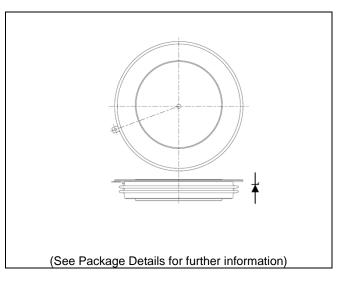


Fig. 1 Package outline M



CURRENT RATINGS

$T_{\text{case}} = 75^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units			
Double Si	Double Side Cooled						
I _{F(AV)}	Mean forward current	Half wave resistive load	8790	А			
I _{F(RMS)}	RMS value	-	13800	Α			
I _F	Continuous (direct) on-state current	-	12777	Α			
Single Sig	Single Side Cooled (Anode side)						
I _{F(AV)}	Mean forward current	Half wave resistive load	5765	А			
I _{F(RMS)}	RMS value	-	9056	Α			
I _F	Continuous (direct) on-state current	-	7698	А			

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units			
Double Sid	Double Side Cooled						
I _{F(AV)}	Mean forward current	Half wave resistive load	6992	Α			
I _{F(RMS)}	RMS value	-	10984	Α			
IF	Continuous (direct) on-state current	-	9942	Α			
Single Sid	Single Side Cooled (Anode side)						
I _{F(AV)}	Mean forward current	Half wave resistive load	4507	Α			
I _{F(RMS)}	RMS value	-	7079	А			
I _F	Continuous (direct) on-state current	-	5857	Α			

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SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	76.0	kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	28.9	MA ² s
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	95.0	kA
l ² t	I ² t for fusing	$V_R = 0$	45.1	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.00558	°C/W
		Single side cooled	Anode DC	-	0.01115	°C/W
			Cathode DC	-	0.01115	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Clamping force 54kN	Double side	-	0.00113	°C/W
		(with mounting compound)	Single side	-	0.00226	°C/W
T_{vj}	Virtual junction temperature	On-state (conducting)		-	170	°C
		Reverse (blocking)		-	160	°C
T _{stg}	Storage temperature range			-55	160	°C
F _m	Clamping force			75	91	kN

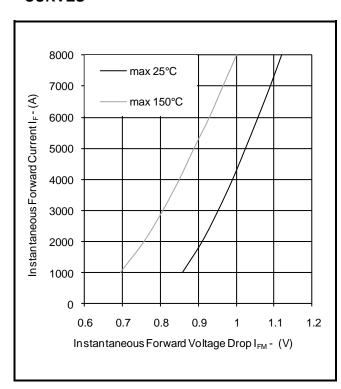
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CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 3000A peak, T _{case} = 25°C	-	0.97	V
I _{RM}	Peak reverse current	At V _{DRM} , T _{case} = 160°C	1	200	mA
Qs	Total stored charge	$I_F = 2000A$, $dI_{RR}/dt = 3A/\mu s$	ı	3900	μC
Irr	Peak reverse recovery current	$T_{case} = 150$ °C, $V_R > 300$ V	1	115	А
V _{TO}	Low Level Threshold voltage	At 150°C, 1000A to 3200A	-	0.6364	V
	High Level Threshold voltage	At 150°C, 3200A to 8000A	-	0.6909	V
	Low Level Slope resistance	At 150°C, 1000A to 3200A		0.056	mΩ
r⊤	High Level Slope resistance	At 150°C, 3200A to 8000A	-	0.0389	mΩ

CURVES



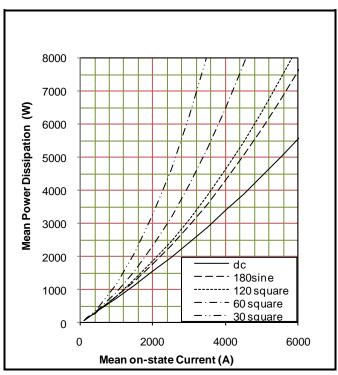


Fig.2 Maximum & minimum on-state characteristics

Fig.3 Dissipation curves

 V_{TM} EQUATION
 Where
 A = 0.419759

 B = 0.029383
 B = 0.0203000023

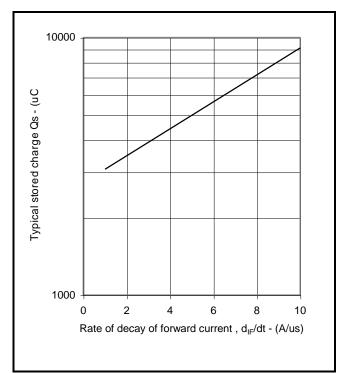
 $V_{TM} = A + Bln (I_T) + C.I_T + D.\sqrt{I_T}$ C = 0.000023

D = 0.001492

these values are valid for $T_j = 150$ °C for $I_T 1000$ A to 8000A

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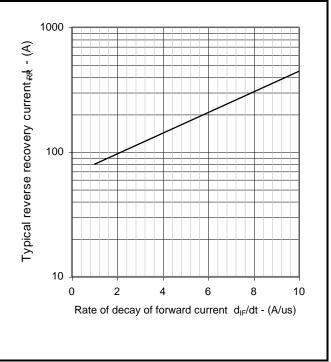


Fig.4 Total stored charge

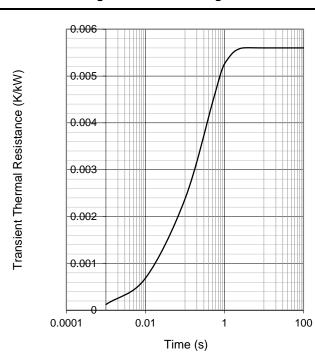


Fig.6 Maximum (limit) transient thermal impedancejunction to case

Fig.5 Maximum reverse recovery current

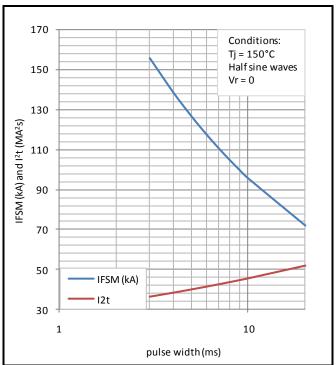


Fig.7 Single cycle surge

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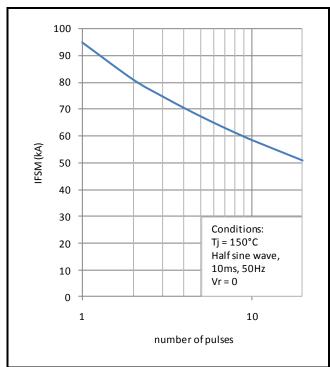


Fig.8 Multi-cycle surge

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PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

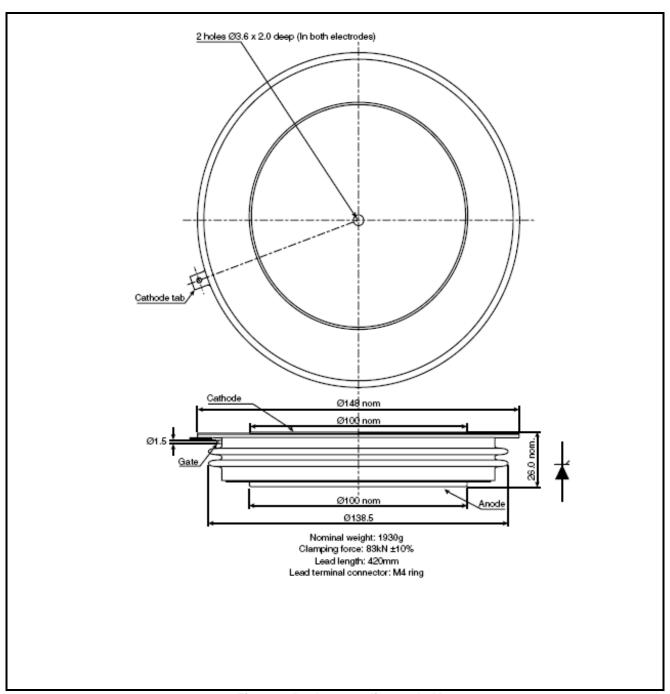


Figure 8 Package outline code M

Note:

Some packages may be supplied with gate and or tags.

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Target Information: This is the most tentative form of information and represents a very preliminary specification.

No actual design work on the product has been started.

Preliminary Information: The product design is complete and final characterisation for volume production is in progress.

The datasheet represents the product as it is now understood but details may change.

No Annotation: The product has been approved for production and unless otherwise notified by Dynex any

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